

AMENDMENTS TO THE CLAIMS

A complete set of the existing claims are set forth below, with the amended claims showing deletions (~~strike-through~~) and insertions (underline).

1. (Currently Amended) In an apparatus, a method of operation comprising:
powering the apparatus from a backup power source, in response to the
apparatus being in an AC absence condition;

initiating, by an Operating System of the apparatus in response to the apparatus
being in the AC absence condition, a suspend to memory process to place the
apparatus in a suspended to memory state wherein an operational state of the
apparatus is saved to volatile memory requiring a source of electrical power to sustain
the suspended to memory state, and wherein no further activity occurs while the
apparatus is in the suspended to memory state including suspension of all data
transmissions; ~~and~~

setting, by a BIOS of the apparatus upon the initiation of the suspend to memory
process, a timer to initiate waking up of the apparatus after a period of time and to
facilitate shutting off the backup power source; ~~and~~

canceled, by the BIOS, the timer as part of a resume process initiated in
response to AC power being re-present at the apparatus.

2. (Previously Presented) The method of claim 1, wherein the timer is a real
time clock (RTC) to initiate waking of the apparatus after the period of time, to facilitate
the shutting off of the backup power source.

3. (Previously Presented) The method of claim 2, wherein the basic
input/output system (BIOS) is adapted to schedule the RTC to initiate waking of the
apparatus after the period of time.

4. (Canceled)

5. (Previously Presented) The method of claim 2, wherein the method further comprises the RTC initiating waking of the apparatus, after passing of the period of time, including as part of waking of the apparatus, the basic input/output system (BIOS) causing the backup power source to be shut off, transitioning the apparatus to an un-powered state instead.

6. (Original) The method of claim 5, wherein the BIOS causes the backup power source to be shut off as part of the waking of the apparatus if AC remains absent.

7. (Canceled)

8. (Canceled)

9. (Previously Presented) The method of claim 1, wherein the method further comprises the basic input/output system (BIOS) canceling the scheduled expiration of the timer as part of a resume process initiated in response to AC returning.

10. (Previously Presented) The method of claim 1, wherein the method further comprises
the timer expiring after passing of the period of time; and
a companion logic of the timer shutting off of the backup power source, placing the apparatus in an un-powered state.

11. (Original) The method of claim 10, wherein the timer shuts off the backup power source if AC remains absent.

12. (Original) The method of claim 1, wherein the method further comprises monitoring for absence of AC to the power supply; and
generating a signal indicating AC absence on detection of absence of AC to the power supply.

13. (Original) The method of claim 12, wherein the monitoring and generating are performed by the power supply.

14. (Previously Presented) The method of claim 1, wherein the method further comprises accepting by the apparatus a specification of the period of time.

15. (Previously Presented) A system comprising:
a power supply to supply power to the system, including a backup power source to supply power during absence of AC to the power supply; and
an arrangement coupled to the power supply to shut off the power supply, after initiating, by an Operating System of the apparatus in response to the apparatus being in an AC absence condition, a suspend to memory process to place the system in a suspended to memory state wherein an operational state of the apparatus is saved to volatile memory requiring a source of electrical power to sustain the suspended to memory state, and wherein no further activity occurs while the system is in the suspended to memory state including suspension of all data transmissions, and after the expiration of a timer set, by a BIOS of the apparatus upon the initiation of the suspend to memory process, to expire after a period of time, the BIOS adapted to cancel the timer as part of a resume process to place the system in an active state in response to AC power re-presence.

16. (Original) The system of claim 15, wherein the arrangement comprises a real time clock (RTC) employable to initiate waking of the system after the period of time, to facilitate shutting off of the backup power source.

17. (Previously Presented) The system of claim 16, wherein the basic I/O system (BIOS) is adapted to schedule the RTC to initiate waking of the system after the period of time.

18. (Canceled)

19. (Previously Presented) The system of claim 16, wherein the basic I/O system (BIOS) is equipped to cause the backup power source to be shut off when the RTC initiates waking of the system.

20. (Original) The system of claim 17, wherein the BIOS is further equipped to cause the backup power source to be shut off if AC remains absent.

21. (Canceled)

22. (Canceled)

23. (Previously Presented) The system of claim 15, wherein the BIOS is further equipped to cancel the scheduled expiration of the timer as part of a resume process to resume the system to an active state in response to AC being re-present at the system.

24. (Original) The system of claim 23, wherein the system further comprises a circuit coupled to the timer to generate a shut off signal to shut off the backup power off, at the expiration of the timer.

25. (Original) The system of claim 24, wherein the circuit is further equipped to receive a AC condition signal indicating whether AC presence or absence, and condition the generation of the shut off signal based on the AC condition signal.

26. (Original) The system of claim 21 wherein the system further comprises a controller to control at least a selected one of an input and an output of the system, and the timer is a part of the controller.

27. (Previously Presented) The system of claim 15, wherein the timer is a part of the power supply.

28. (Original) The system of claim 15, wherein the arrangement is further equipped to accept for the system a specification of the period of time.

29. (Original) The system of claim 15, wherein the system further comprises a networking interface.

30. (Currently Amended) A power supply comprising:
an output interface;
a backup power source; and
a switch conditionally coupling the integral backup power source to the output interface to output power through the output interface during absence of AC to the power supply, including a control interface accessible during a suspended to memory state of a host device hosting the power supply to allow the backup power source to be uncoupled from the output interface to stop the backup power source from outputting power through the output interface after the host device has entered the suspended to memory state, during which state no data are transmitted, said suspended to memory state initiated by an Operating System of the host device in response to the AC absence condition and wherein an operational state of the host device is saved to volatile memory requiring a source of electrical power to sustain the suspended to memory state and further after the expiration of a timer set, by a BIOS of the host device upon the initiation of the suspend to memory process, to expire after a period of time, the BIOS adapted to cancel the timer as part of a resume process to place the system in an active state in response to AC power re-presence.

31. (Original) The power supply of claim 30, wherein the power supply further comprises a monitor to monitor for presence or absence of AC to the power supply, and to generate a signal indicating the presence or absence of AC accordingly.

32. (Previously Presented) The power supply of claim 30, wherein the power supply further comprises the timer settable to expire after the period of time to shut off the backup power source.

33. (Currently Amended) An article of manufacture comprising:
a storage medium;
a plurality of programming instruction stored therein, designed to enable an apparatus to be able to perform, when the apparatus is in an AC absence condition:
setting a real time clock (RTC) to initiate waking of the apparatus after the apparatus has been placed into a suspended to memory state initiated by an Operating System of the apparatus in response to the AC absence condition, under which state no data are transmitted, and wherein an operational state of the apparatus is saved to volatile memory requiring a source of electrical power to sustain the suspended to memory state and to shut off a backup power source of the apparatus upon the expiration of a timer set, ~~by a BIOS of the apparatus upon the initiation of the suspend to memory process, to expire after a period of time; and-~~
cancel the timer as part of a resume process to put the apparatus in an active state initiated in response to AC power being re-present at the apparatus.

34. (Previously Presented) The article of claim 33, wherein the programming instructions are further designed to enable the apparatus to perform the setting operation, when intervening in a process to suspend the apparatus.

35. (Canceled)

36. (Original) The article of claim 33, wherein the programming instructions are further designed to enable the apparatus to shut off the backup power source when the RTC initiates waking of the apparatus after passing of the time period.

37. (Original) The article of claim 36, wherein the programming instructions are further designed to enable the apparatus to perform the shut off conditioned on AC remains absent at the apparatus.

38. (Previously Presented) The article of claim 33, wherein the programming instructions implement the enabling of the apparatus to perform f the setting operation as part of the basic input/output system (BIOS).